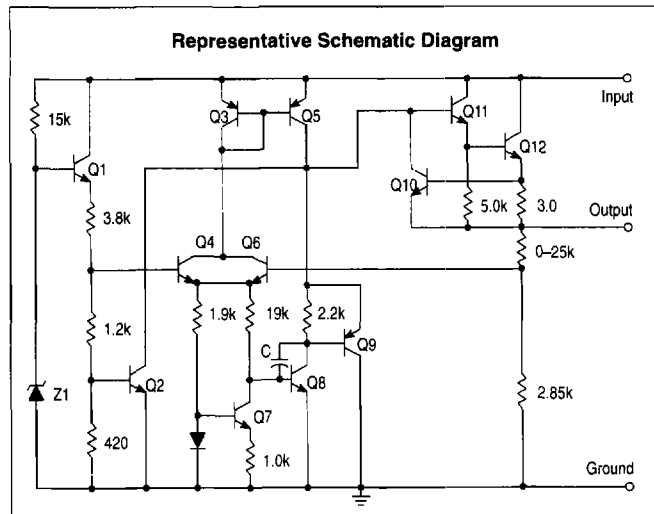


# Three-Terminal Low Current Positive Voltage Regulators

The MC78L00, A Series of positive voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100 mA. Like their higher powered MC7800 and MC78M00 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the MC78L00 devices in many applications.

These devices offer a substantial performance advantage over the traditional zener diode-resistor combination, as output impedance and quiescent current are substantially reduced.

- Wide Range of Available, Fixed Output Voltages
- Low Cost
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required
- Complementary Negative Regulators Offered (MC79L00 Series)
- Available in either  $\pm 5\%$  (AC) or  $\pm 10\%$  (C) Selections



## ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC78LXXACD*	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	SOP-8
MC78LXXACP		Plastic Power
MC78LXXCP		Plastic Power
MC78LXXABD*	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	SOP-8
MC78LXXABP*		Plastic Power

XX indicates nominal voltage

\*Available in 5, 8, 9, 12 and 15 V devices.

## MC78L00, A Series

**P SUFFIX**  
CASE 29

Pin 1. Output  
2. GND  
3. Input



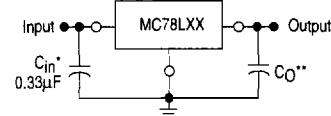
**D SUFFIX**  
PLASTIC PACKAGE  
CASE 751  
(SOP-8)\*



Pin 1.  $V_{out}$   
2. GND  
3. GND  
4. NC  
5. NC  
6. GND  
7. GND  
8.  $V_{in}$

\*SOP-8 is an internally modified SO-8 package. Pins 2, 3, 6, and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipation capability when appropriately mounted on a printed circuit board. SOP-8 conforms to all external dimensions of the standard SO-8 package.

## Standard Application



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

\* $C_{in}$  is required if regulator is located an appreciable distance from power supply filter.

\*\* $C_O$  is not needed for stability; however, it does improve transient response.

## DEVICE TYPE/NOMINAL VOLTAGE

		10%	5%	Voltage
MC78L05C	MC78L05AC			5.0
MC78L08C	MC78L08AC			8.0
MC78L09C	MC78L09AC			9.0
MC78L12C	MC78L12AC			12
MC78L15C	MC78L15AC			15
MC78L18C	MC78L18AC			18
MC78L24C	MC78L24AC			24

## MC78L00, A Series

**MAXIMUM RATINGS** ( $T_A = +125^\circ\text{C}$ , unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (2.6 V–8.0 V) (12 V–18 V) (24 V)	$V_I$	30 35 40	Vdc
Storage Temperature Range	$T_{\text{stg}}$	–65 to +150	$^\circ\text{C}$
Operating Junction Temperature Range	$T_J$	0 to +150	$^\circ\text{C}$

3

**ELECTRICAL CHARACTERISTICS** ( $V_I = 10\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $-40^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAB),  $0^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAC), unless otherwise noted.)

Characteristics	Symbol	MC78L05AC, AB			MC78L05C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	4.8	5.0	5.2	4.6	5.0	5.4	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $7.0\text{ Vdc} \leq V_I \leq 20\text{ Vdc}$ $8.0\text{ Vdc} \leq V_I \leq 20\text{ Vdc}$	Reg <sub>line</sub>	–	55 45	150 100	–	55 45	200 150	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Reg <sub>load</sub>	–	11 5.0	60 30	–	11 5.0	60 30	mV
Output Voltage ( $7.0\text{ Vdc} \leq V_I \leq 20\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 10\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	4.75 4.75	–	5.25 5.25	4.5 4.5	–	5.5 5.5	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{\text{IB}}$	–	3.8 –	6.0 5.5	–	3.8 –	6.0 5.5	mA
Input Bias Current Change ( $8.0\text{ Vdc} \leq V_I \leq 20\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{\text{IB}}$	–	–	1.5 0.1	–	–	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	40	–	–	40	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $8.0\text{ Vdc} \leq V_I \leq 18\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	41	49	–	40	49	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

**ELECTRICAL CHARACTERISTICS** ( $V_I = 14\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $-40^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAB),  $0^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAC), unless otherwise noted.)

Characteristics	Symbol	MC78L08AC, AB			MC78L08C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	7.7	8.0	8.3	7.36	8.0	8.64	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $10.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$ $11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$	Reg <sub>line</sub>	–	20 12	175 125	–	20 12	200 150	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Reg <sub>load</sub>	–	15 8.0	80 40	–	15 6.0	80 40	mV
Output Voltage ( $10.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 14\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	7.6 7.6	–	8.4 8.4	7.2 7.2	–	8.8 8.8	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{\text{IB}}$	–	3.0 –	6.0 5.5	–	3.0 –	6.0 5.5	mA
Input Bias Current Change ( $11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{\text{IB}}$	–	–	1.5 0.1	–	–	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	60	–	–	52	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $12\text{ V} \leq V_I \leq 23\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	37	57	–	36	55	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

## MC78L00, A Series

**ELECTRICAL CHARACTERISTICS** ( $V_I = 15\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $-40^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAB),  $0^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAC), unless otherwise noted.)

Characteristics	Symbol	MC78L09AC, AB			MC78L09C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	8.6	9.0	9.4	8.3	9.0	9.7	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $11.5\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$ $12\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$	Reg <sub>line</sub>	– –	20 12	175 125	– –	20 12	200 150	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Reg <sub>load</sub>	– –	15 8.0	90 40	– –	15 6.0	90 40	mV
Output Voltage ( $11.5\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 15\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	8.5 8.5	– –	9.5 9.5	8.1 8.1	– –	9.9 9.9	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{IB}$	– –	3.0 –	6.0 5.5	– –	3.0 –	6.0 5.5	mA
Input Bias Current Change ( $11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{IB}$	– –	– –	1.5 0.1	– –	– –	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	60	–	–	52	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $13\text{ V} \leq V_I \leq 24\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	37	57	–	36	55	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

**ELECTRICAL CHARACTERISTICS** ( $V_I = 19\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $-40^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAB),  $0^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAC), unless otherwise noted.)

Characteristics	Symbol	MC78L12AC, AB			MC78L12C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	11.5	12	12.5	11.1	12	12.9	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $14.5\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$ $16\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$	Reg <sub>line</sub>	– –	120 100	250 200	– –	120 100	250 200	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Reg <sub>load</sub>	– –	20 10	100 50	– –	20 10	100 50	mV
Output Voltage ( $14.5\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 19\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	11.4 11.4	– –	12.6 12.6	10.8 10.8	– –	13.2 13.2	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{IB}$	– –	4.2 –	6.5 6.0	– –	4.2 –	6.5 6.0	mA
Input Bias Current Change ( $16\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{IB}$	– –	– –	1.5 0.1	– –	– –	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	80	–	–	80	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $15\text{ V} \leq V_I \leq 25\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	37	42	–	36	42	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

## MC78L00, A Series

**ELECTRICAL CHARACTERISTICS** ( $V_I = 23\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $-40^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAB),  $0^\circ\text{C} < T_J < +125^\circ\text{C}$  (for MC78LXXAC), unless otherwise noted.)

Characteristics	Symbol	MC78L15AC, AB			MC78L15C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	14.4	15	15.6	13.8	15	16.2	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$ $20\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$	Regline	–	130	300	–	130	300	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Regload	–	25	150	–	25	150	mV
Output Voltage ( $17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 23\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	14.25 14.25	– –	15.75 15.75	13.5 13.5	– –	16.5 16.5	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{IB}$	– –	4.4 –	6.5 6.0	– –	4.4 –	6.5 6.0	mA
Input Bias Current Change ( $20\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{IB}$	– –	– –	1.5 0.1	– –	– –	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	90	–	–	90	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $18.5\text{ V} \leq V_I \leq 28.5\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	34	39	–	33	39	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

**ELECTRICAL CHARACTERISTICS** ( $V_I = 27\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $0^\circ\text{C} < T_J < +125^\circ\text{C}$ , unless otherwise noted.)

Characteristics	Symbol	MC78L18AC			MC78L18C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	17.3	18	18.7	16.6	18	19.4	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $21.4\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $20.7\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $22\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$	Regline	–	45	325	–	32	325	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Regload	–	30	170	–	30	170	mV
Output Voltage ( $21.4\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $20.7\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $V_I = 27\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ ) ( $V_I = 27\text{ V}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	17.1 17.1	– –	18.9 18.9	16.2 16.2	– –	19.8 19.8	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{IB}$	– –	3.1 –	6.5 6.0	– –	3.1 –	6.5 6.0	mA
Input Bias Current Change ( $22\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ ) ( $21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{IB}$	– –	– –	1.5 0.1	– –	– –	1.5 0.2	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	150	–	–	150	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $23\text{ V} \leq V_I \leq 33\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	33	48	–	32	46	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

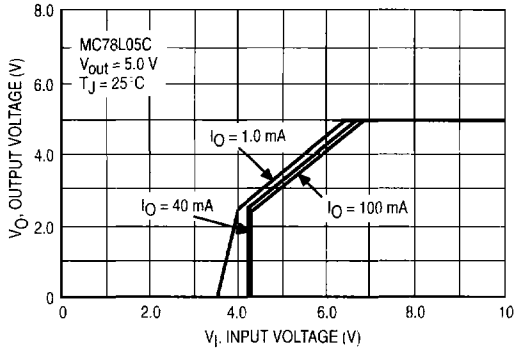
## MC78L00, A Series

**ELECTRICAL CHARACTERISTICS** ( $V_I = 33\text{ V}$ ,  $I_O = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $0^\circ\text{C} < T_J < +125^\circ\text{C}$ , unless otherwise noted.)

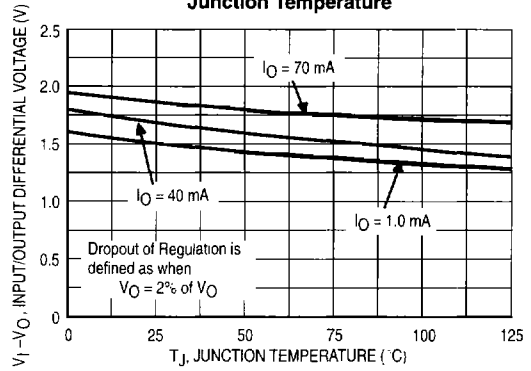
Characteristics	Symbol	MC78L24AC			MC78L24C			Unit
		Min	Typ	Max	Min	Typ	Max	
Output Voltage ( $T_J = +25^\circ\text{C}$ )	$V_O$	23	24	25	22.1	24	25.9	Vdc
Line Regulation ( $T_J = +25^\circ\text{C}$ , $I_O = 40\text{ mA}$ ) $27.5\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$ $28\text{ Vdc} \leq V_I \leq 80\text{ Vdc}$ $27\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$	Regline	–	–	–	–	35	350	mV
Load Regulation ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$ ) ( $T_J = +25^\circ\text{C}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	Regload	–	40	200	–	40	200	mV
Output Voltage ( $28\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $27\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ ) ( $28\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ ) ( $27\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ , $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$ )	$V_O$	22.8	–	25.2	21.6	–	26.4	Vdc
Input Bias Current ( $T_J = +25^\circ\text{C}$ ) ( $T_J = +125^\circ\text{C}$ )	$I_{IB}$	–	3.1	6.5	–	3.1	6.5	mA
Input Bias Current Change ( $28\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$ ) ( $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$ )	$\Delta I_{IB}$	–	–	1.5	–	–	1.5	mA
Output Noise Voltage ( $T_A = +25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$ )	$V_n$	–	200	–	–	200	–	$\mu\text{V}$
Ripple Rejection ( $I_O = 40\text{ mA}$ , $f = 120\text{ Hz}$ , $29\text{ V} \leq V_I \leq 35\text{ V}$ , $T_J = +25^\circ\text{C}$ )	RR	31	45	–	30	43	–	dB
Dropout Voltage ( $T_J = +25^\circ\text{C}$ )	$V_I - V_O$	–	1.7	–	–	1.7	–	Vdc

# MC78L00, A Series

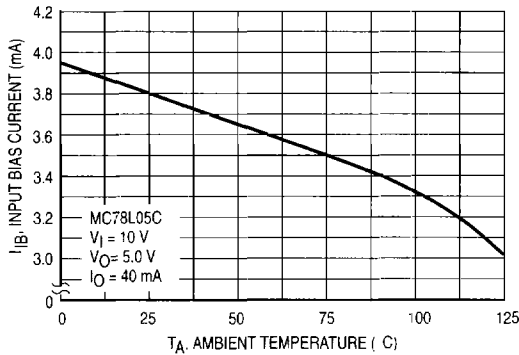
**Figure 1. Dropout Characteristics**



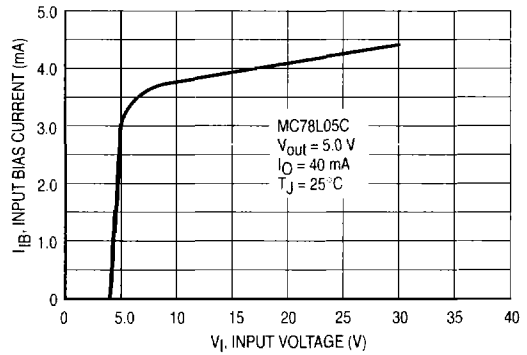
**Figure 2. Dropout Voltage versus Junction Temperature**



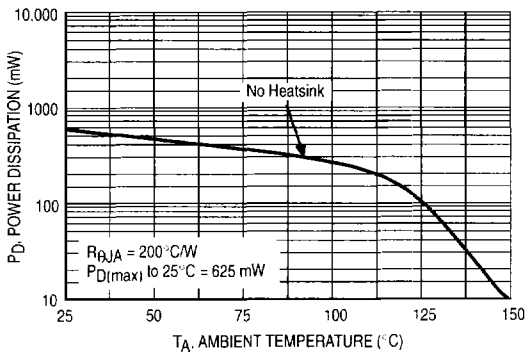
**Figure 3. Input Bias Current versus Ambient Temperature**



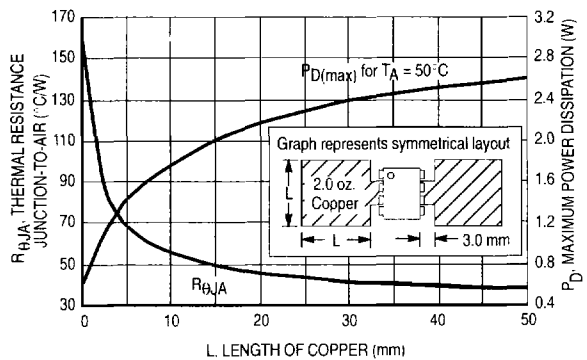
**Figure 4. Input Bias Current versus Input Voltage**



**Figure 5. Maximum Average Power Dissipation versus Ambient Temperature – TO-92 Type Package**



**Figure 6. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length**



# MC78L00, A Series

## APPLICATIONS INFORMATION

### Design Considerations

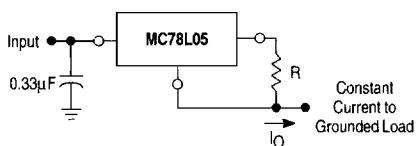
The MC78L00 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. The input

bypass capacitor should be selected to provide good high-frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.

3

Figure 7. Current Regulator



The MC78L00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC78L05C is chosen in this application. Resistor R determines the current as follows:

$$I_O = \frac{5.0V}{R} + I_B$$

$I_B = 3.8 \text{ mA}$  over line and load changes

For example, a 100 mA current source would require R to be a 50 Ω, 1/2 W resistor and the output voltage compliance would be the input voltage less 7 V.

Figure 8. ± 15 V Tracking Voltage Regulator

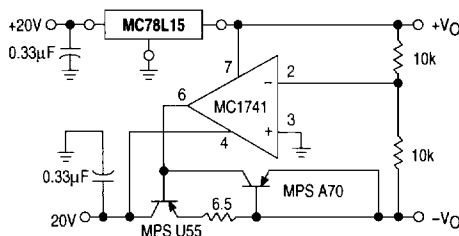


Figure 9. Positive and Negative Regulator

